

Department of Electrical and Computer Engineering Degree Audit Form

Student Name:		Student Number:	
Student Email:			
Semester Seeking Registration:		Audit Calendar:	
Registered in Co-Op Program:	<input type="checkbox"/> Yes / <input type="checkbox"/> No	GPA:	
Transfer Credits:	<input type="checkbox"/> Yes / <input type="checkbox"/> No	Semester Level:	

1st Year Courses	<input type="checkbox"/> All Ten (10) Courses Completed?	<input checked="" type="checkbox"/> Completed Courses; <input type="checkbox"/> Courses In Progress
Fall Term	Winter Term	Summer Term
<input type="checkbox"/> GENG-1101 (Engineering I)	<input type="checkbox"/> CHEM-1103 (Topics in General Chemistry)	
<input type="checkbox"/> GENG-1102 (Engineering Graphics)	<input type="checkbox"/> GENG-1200 (Engineering Thermofluids)	
<input type="checkbox"/> GENG-1110 (Engineering Mechanics I)	<input type="checkbox"/> GENG-1201 (Cornerstone Design)	
<input type="checkbox"/> MATH-1270 (Linear Algebra)	<input type="checkbox"/> MATH-1730 (Integral Calculus)	
<input type="checkbox"/> MATH-1720 (Differential Calculus)	<input type="checkbox"/> PHYS-1410 (Intro Physics II)	

2nd Year Courses	<input type="checkbox"/> All Twelve (12) Courses Completed?	<input checked="" type="checkbox"/> Completed Courses; <input type="checkbox"/> Courses In Progress
Students must have completed at least eight (8) of their 1st year courses before being allowed to register into the 2nd year courses including all pre-requisite courses required for registration into the 2nd year courses. One (1) electives outside of Engineering is also required.		
Fall Term	Winter Term	Summer Term
<input type="checkbox"/> GENG-2320 (Eng. Software Fundamentals)	<input type="checkbox"/> ELEC-2170 (Digital Logic Design I) [co req: MATH-2790]	<input type="checkbox"/> GENG-2980 (Co Op Work Term I)
<input type="checkbox"/> GENG-2340 (Electrical & Comp. Fund.) [pre req: PHYS-1410] [No credit for both GENG/ELEC-1240 and GENG/ELEC-2140]	<input type="checkbox"/> ELEC-2200 (Circuit Analysis) [pre req: GENG-2340]	
<input type="checkbox"/> MATH-2780 (Vector Calculus)	<input type="checkbox"/> ELEC-2240 (Signals and Systems) [co req: MATH-2780] [pre req MATH-2790]	
<input type="checkbox"/> MATH-2790 (Differential Equations)	<input type="checkbox"/> ELEC-2260 (Electronics I) [co reqs: ELEC-2240 & MATH-2780] [pre req: MATH -2790]	
<input type="checkbox"/> PHYS-2200 (Oscillations, Waves, and Electromagnetism)	<input type="checkbox"/> ELEC-2280 (Electromagnetic Fields) [co req: MATH-2780] [pre req: MATH-2790]	
<input type="checkbox"/> One non-technical course from the Faculty of Engineering's approved list.	<input type="checkbox"/> GENG-2220 (Treatment of Exp. Data) [pre req: MATH-1720]	

3rd Year Courses	<input type="checkbox"/> All Eleven (11) Courses Completed?	<input checked="" type="checkbox"/> Completed Courses; <input type="checkbox"/> Courses In Progress
Students must have completed all the 1st year courses and at least ten (10) of their 2nd year courses before being allowed to register into the 3rd year courses including all pre-requisite courses required for registration into the 3rd year courses. One (1) electives outside of Engineering is also required.		
Fall Term	Winter Term	Summer Term
<input type="checkbox"/> ELEC-3010 (Comp-Aided Analysis) [co req: MATH-2780, MATH-2790]	<input type="checkbox"/> GENG-3980 (Co Op Work Term II)	<input type="checkbox"/> ELEC-3030 (Physical Electronics) [pre req MATH-2780, MATH-2790]
<input type="checkbox"/> ELEC-3130 (Electromechanical Systems) [pre reqs: MATH-2780, MATH-2790, ELEC-2250]		<input type="checkbox"/> ELEC-3240 (Control Systems I) [pre req MATH-2780, MATH-2790, ELEC-3130]
<input type="checkbox"/> ELEC-3160 (Electronics II) [pre reqs: MATH-2780, MATH-2790, ELEC-2260]		<input type="checkbox"/> ELEC-3270 (Microprocessors) [pre reqs MATH-2780, MATH-2790, ELEC-3160, ELEC-3300]
<input type="checkbox"/> ELEC-3300 (Dig. Logic Design II) [pre reqs: MATH-2780, MATH-2790, ELEC-2170]		<input type="checkbox"/> ELEC-3290 (Analog Comm.) [pre reqs: MATH-2780, MATH-2790, ELEC-3160]
<input type="checkbox"/> GENG-3130 (Engineering Economy)		<input type="checkbox"/> GENG-2500 (Engineering & the Environment)
		<input type="checkbox"/> One non-technical course from the Faculty of Engineering's approved list.

4th Year Courses	<input type="checkbox"/> All Eleven (11) Courses Completed?	<input checked="" type="checkbox"/> Completed Courses; <input type="checkbox"/> Courses In Progress
Students cannot register into any of the 4th year courses until all Electrical Engineering courses from 1st, 2nd and 3rd year have been completed. A total of five (5) ECE electives need to be completed.		
Fall Term	Winter Term – Core Courses	Summer Term - Core Courses
<input type="checkbox"/> GENG-4980 (Co Op Work Term III)	<input type="checkbox"/> ELEC-4000A (Capstone Design)	<input type="checkbox"/> ELEC-4000B (Capstone Design)
	<input type="checkbox"/> ELEC-4310 (Control Systems II)	<input type="checkbox"/> ELEC-4320 (EM Waves & Radiating Systems)
	<input type="checkbox"/> ELEC-4570 (Fundamentals of DSP)	<input type="checkbox"/> GENG-4210 (Engineering & Society)
	Winter Elective Courses:	Summer Elective Courses:
	<input type="checkbox"/> ELEC-4190 (Digital Comm.)	<input type="checkbox"/> ELEC-4330 (Dig. Int. Circuits)
	<input type="checkbox"/> ELEC-4350 (Microelectromechanical Systems)	<input type="checkbox"/> ELEC-4340 (Automotive Electronics)
	<input type="checkbox"/> ELEC-4360 (Computer Comm.)	<input type="checkbox"/> ELEC-4380 (Coding & Info Theory)
	<input type="checkbox"/> ELEC-4370 (Intelligent Computing)	<input type="checkbox"/> ELEC-4390 (Multimedia Systems)
	<input type="checkbox"/> ELEC-4440 (Analog Integrated Circuits)	<input type="checkbox"/> ELEC-4400 (Wireless Comm.)
	<input type="checkbox"/> ELEC-4450 (Power Electronics)	<input type="checkbox"/> ELEC-4470 (Computer Network Security)
	<input type="checkbox"/> ELEC-4490 (Sensor and Vision Systems)	<input type="checkbox"/> ELEC-4480 (Digital Comp. Arch.)
	<input type="checkbox"/> ELEC-4500 (Power Systems I)	<input type="checkbox"/> ELEC-4600 (Power Systems II)